

WEST Search History

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DATE: Friday, September 16, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L38	5305452[uref]	25
<input type="checkbox"/>	L37	5305452.pn. and power	0
<input type="checkbox"/>	L36	5305452.pn.	1
<input type="checkbox"/>	L35	5754867[uref]	3
<input type="checkbox"/>	L34	l33 same (power or consumption)	6
<input type="checkbox"/>	L33	(different) near3 frequency near5 ((cpu or \$processor) near5 (bus or bridge))	44
<input type="checkbox"/>	L32	(different) near3 frequency ((cpu or \$processor) near5 (bus or bridge))	1
<input type="checkbox"/>	L31	(first or low) near5 (second or high) near3 frequency ((cpu or \$processor) near5 (bus or bridge))	1
<input type="checkbox"/>	L30	L29 and power	0
<input type="checkbox"/>	L29	6668292.pn.	1
<input type="checkbox"/>	L28	6728890.pn. and clock a	1
<input type="checkbox"/>	L27	6496938[uref]	1
<input type="checkbox"/>	L26	6240522[uref]	4
<input type="checkbox"/>	L25	6065122[uref]	10
<input type="checkbox"/>	L24	adjust\$ near5 clock near5 ((cpu or \$processor) near5 (bus or bridge))	18
<input type="checkbox"/>	L23	adjust\$ near5 clock near5 ((cpu or \$processor) near5 (bus or bridge)) near5 separat\$	1
<input type="checkbox"/>	L22	adjust\$ near5 clock near5 ((cpu or \$processor) near5 (bus or bridge)) near5 independ\$	0
<input type="checkbox"/>	L21	6704879.pn. and clock	1
<input type="checkbox"/>	L20	(first or low) near5 (second or high) near5 frequency near10 ((cpu or \$processor) near5 bridge)	3
<input type="checkbox"/>	L19	L16 same ((cpu or \$processor) near15 bridge)	0
<input type="checkbox"/>	L18	L16 same ((cpu or \$processor) near5 bridge)	0
<input type="checkbox"/>	L17	L16 same power	37
<input type="checkbox"/>	L16	l1 near5 frequency	421
<input type="checkbox"/>	L15	L13 near5 adjust\$	3
<input type="checkbox"/>	L14	L13 same power	1
<input type="checkbox"/>	L13	l1 near5 (independ\$)	29
<input type="checkbox"/>	L12	L11 near10 power	4

<input type="checkbox"/>	L11	11 near5 (distinct or different)	78
<input type="checkbox"/>	L10	19 and power	5
<input type="checkbox"/>	L9	(first or low) near5 (second or high)near5 clock near10 (cpu or \$processor) near5 bridge	9
<input type="checkbox"/>	L8	L1 near10 (cpu or \$processor) near5 bridge	1
<input type="checkbox"/>	L7	L1 near10 (battery near5 a\$1c) (4908523 5115503 5319772 5369771 5513340 5581693 5600839 5628019	2
<input type="checkbox"/>	L6	5835750 5987614 6118306 6292430 6457135 6477609 6530001 6564332)![pn]	16
<input type="checkbox"/>	L5	6763478[uref]	3
<input type="checkbox"/>	L4	L1 near10 (battery)	4
<input type="checkbox"/>	L3	L2.ti,ab.	14
<input type="checkbox"/>	L2	L1 near10 (battery or mode)	101
<input type="checkbox"/>	L1	clock generat\$ near5 (first or low) near5 (second or high)	2458

END OF SEARCH HISTORY